



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/829,100

Filing Date:

April 21, 2004

Applicant:

Masaru Mori

Group Art Unit:

2816

Examiner:

To be assigned

Title:

CLOCK SIGNAL DETECTION CIRCUIT AND

SEMICONDUCTOR INTEGRATED CIRCUIT USING

THE SAME

Attorney Docket:

9319S-000748

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Sir:

We acknowledge receipt of the Updated Filing Receipt (copy enclosed) for the above-identified patent application. However, the title information contains an error (shown in bold and underscore below). Please correct your records to show the correct title as indicated.

Error: "Clock siginal detection..."

Correction: "Clock signal detection...."

Thank you for your assistance.

Respectfully submitted,

By:

Dated: August 23, 2004

Rea. No. 27.882

Harness, Dickey & Pierce, P.L.C.

P.O. Box 828

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United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Vinginia 22313-1450

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/829.100	04/21/2004	2816	0.00	9319S-000748	3	8	4

CONFIRMATION NO. 6000

27572 HARNESS, DICKEY & PIERCE, P.L.C. P.O. BOX 828 BLOOMFIELD HILLS, MI 48303 FILING RECEIPT

OC00000013088795

Date Mailed: 06/28/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Masaru Mori, Suwa, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2003-115248 04/21/2003

If Required, Foreign Filing License Granted: 06/27/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Clock signal detection circuit and semiconductor integrated circuit using the same

signal

Preliminary Class

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